

(Use as many sheets as necessary)

Sheet	1	of	2
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Application Number	10/017,777
Filing Date	December 13, 2001
First Named Inventor	Kuzemchak
Art Unit	2122
Examiner Name	Vo
Attorney Docket Number	T1032443

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Examiner Signature	TED T. VO
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Date Considered	04/12/05
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Application Number	10/017,111		
		Filing Date	December 13, 2001		
		First Named Inventor	Kuzemchak		
		Art Unit	2122		
		Examiner Name	Vo		
Sheet	2	of	2	Attorney Docket Number	TI-32443

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
FTV		Bryant et al., "Verification of Pipelined Microprocessors by Comparing Memory Execution Sequences in Symbolic Simulation" ASIAN '97 (1997), pp. 1-14.	

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